



IFW

Attorney Docket No. 5646-107

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: David Gibson et al.

Group Art Unit: 2655

Serial No.: 10/774,904

Confirmation No.: 8774

Filed: February 9, 2004

For: DOUBLE DATA RATE MEMORY DEVICES INCLUDING CLOCK DOMAIN
ALIGNMENT CIRCUITS AND METHODS OF OPERATION THEREOF

Date: August 11, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

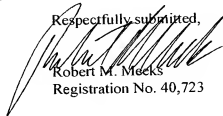
INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP. No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,



Robert M. Meeks
Registration No. 40,723

Myers Bigel Sibley & Sajovec, P.A.
P. O. Box 37428
Raleigh, North Carolina 27627
Telephone: (919) 854-1400
Facsimile: (919) 854-1401
Customer No. 20792

Certificate of Mailing under 37 CFR 1.8 (or 1.10)

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 11, 2004.



Candi L. Riggs

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)



Complete if Known

| | |
|------------------------|-----------------------|
| Application Number | To Be Assigned |
| Filing Date | Concurrently Herewith |
| First Named Inventor | David Gibson |
| Group Art Unit | 2655 |
| Examiner Name | |
| Attorney Docket Number | 5646-107 |

| | |
|-------|----|
| Sheet | A1 |
|-------|----|

U.S. PATENTS AND PATENT PUBLICATIONS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T |
|--------------------|----------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| | 1 | "18 Mb Pipelined QDR™ I SRAM Burst of 2, Advance Information" Integrated Device Technology, Inc., August 2003, 20 pages | |
| | 2 | "Quad Data Rate™ (QDR™) SRAM Clocking Scheme," Cypress Semiconductor Corporation, February 16, 2000, 2 pages | |
| | 3 | "QDR™ III SRAM: A Design Guide," Cypress Semiconductor Corporation, June 12, 2002, 6 pages | |
| | 4 | "DDR2 Offers New Features and Functionality," designline, vol. 12, issue 2, 3 rd Quarter 2003, 16 pages | |
| | 5 | "DDR2 SDRAM, 256Mb: x4, x8, x16 DDR2 SDRAM," Micron Technology, Inc., 2003, 99 pages | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

Examiner Signature _____

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.